

Abstracts

A 20 W 2080-2200 MHz hybrid power amplifier optimized for error amplifier applications

E.J. Crescenzi, Jr.. "A 20 W 2080-2200 MHz hybrid power amplifier optimized for error amplifier applications." 2002 MTT-S International Microwave Symposium Digest 02.2 (2002 Vol. II [MWSYM]): 613-616 vol.2.

A hybrid power amplifier module has been designed for application as an error amplifier in feedforward PAs for UNITS multi-channel operation. Error amplifiers require excess bandwidth, excellent gain flatness, and high linearity. Si LDMOS power devices were integrated with chip-and-wire matching and hybrid SMT amplifier circuitry chosen to optimize these characteristics.

[Return to main document.](#)